

# Alston & Bird LLP

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January 10, 2011

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: United States Patent for *Optimizing Switching Element For Minimal Latency*  
Application No. 10/082,450; Filed February 23, 2002  
Patent No. 7,609,695; Issued October 27, 2009  
Our File 054788/347548

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Sir:

It is respectfully requested that a Certificate of Correction be issued for the above-identified patent, in accordance with 37 C.F.R. §1.322. This request is made in order to correct the mistakes incurred through the fault of the United States Patent and Trademark Office ("USPTO").

The mistakes appearing in the patent are set forth on the Certificate of Correction e-filed herewith.

Respectfully submitted,



Andrew T. Spence  
Registration No. 45,699

ATS:mf  
Enclosure

LEGAL02/32399084v1

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,609,695 B2

Page 1 of 1

APPLICATION NO. : 10/082,450

ISSUE DATE : October 27, 2009

INVENTOR(S) : Zhu et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 94,

Line 32, "different from the a bit" should read --different from a bit--.

MAILING ADDRESS OF SENDER

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